## VAR-SOM-MX8X Yocto Sumo v1.5 Release Notes

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|---|---|
| Based on release | Yocto: Poky 2.5, BSP: NXP Sumo BSP L4.14.98-2.0.0_ga, Linux: Freescale imx_4.14.98-2.0.0_ga |
| Nature of release | Variscite Yocto Sumo i.MX8QXP Release v1.5 |
| Date | 3/20/2020 |
| Supported platform | VAR-SOM-MX8X |
| SOM Revision | v1.1 and higher |
| Carrier board revision | v1.1 and higher |
| Variscite BSP git | https://github.com/varigit/meta-variscite-imx |
| Variscite BSP branch | sumo-imx-4.14.98-var01 |
| Variscite BSP commit ID | 8124fd9dd900ff59db04e38aebb311658e39b5d5 |
| Kernel git | https://github.com/varigit/linux-imx |
| Kernel branch | imx_4.14.98_2.0.0_ga_var01 |
| Kernel commit ID | 23721d2185ae5f257a67ea27940767b6be1f2d71 |
| U-Boot git | https://github.com/varigit/uboot-imx |
| U-Boot branch | imx_v2018.03_4.14.98_2.0.0_ga_var01 |
| U-Boot commit ID | 42342c4846a45a950e34bfae198e5e2b19ef6c6e |
| File System build system | Yocto Sumo |
| Recovery SD card link | var-som-mx8x-recovery-sd.v15.img.gz |
| **Changes list** | **Description** |
| Release 1.5 | |
| Added SCFW support for reading DRAM configuration from EEPROM | Required to support various DRAM sizes and configurations using a single boot image |
| Enabled U-Boot SPL | Required for customized DRAM configurations in SC firmware |
| Enabled U-Boot support for reading DRAM size from EEPROM | Instead of hardcoding DRAM size in U-Boot configuration, read actual DRAM size from EEPROM |
| Fixed alsactl store/restore sequence | alsactl -f state.conf store/restore sequence was failing due to driver bug, fixed now |
| Release 1.3 | |
| Fixed Bluetooth suspend/resume sequence | |
| Added support for reading extended P/N from EEPROM | |
| Added EEPROM support to SC firmware | |
| Release 1.1 | |
| Added SWUpdate example recipes | |
| Added support for OV5640 parallel CSI sensor | |
| Disabled internal RTC | |
| Release 1.0 | |
| Initial Yocto Release | |